

Fig. 1A

100

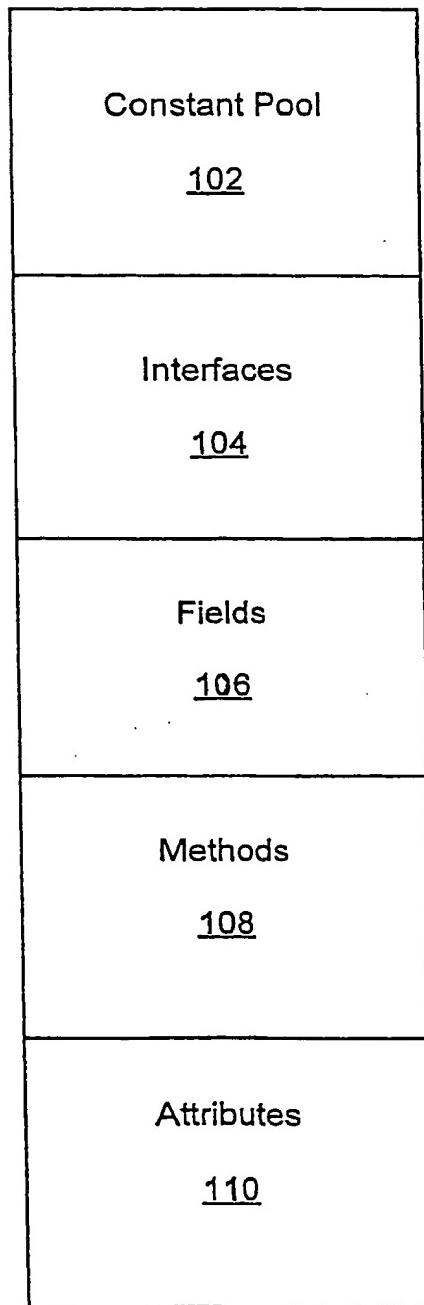


Fig. 1B

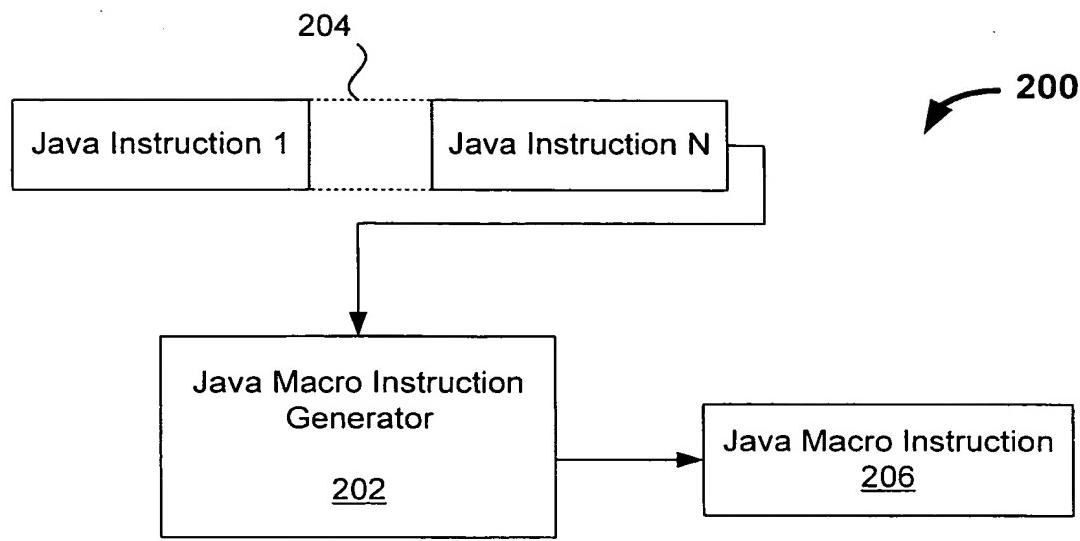


Fig. 2A

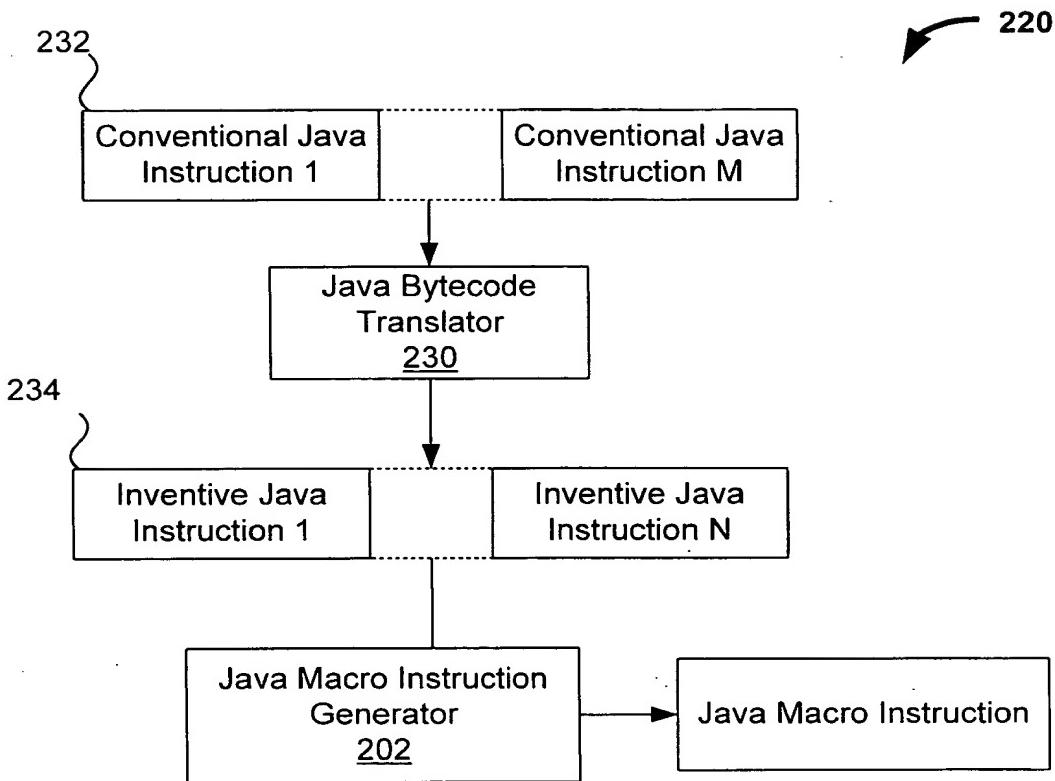


Fig. 2B

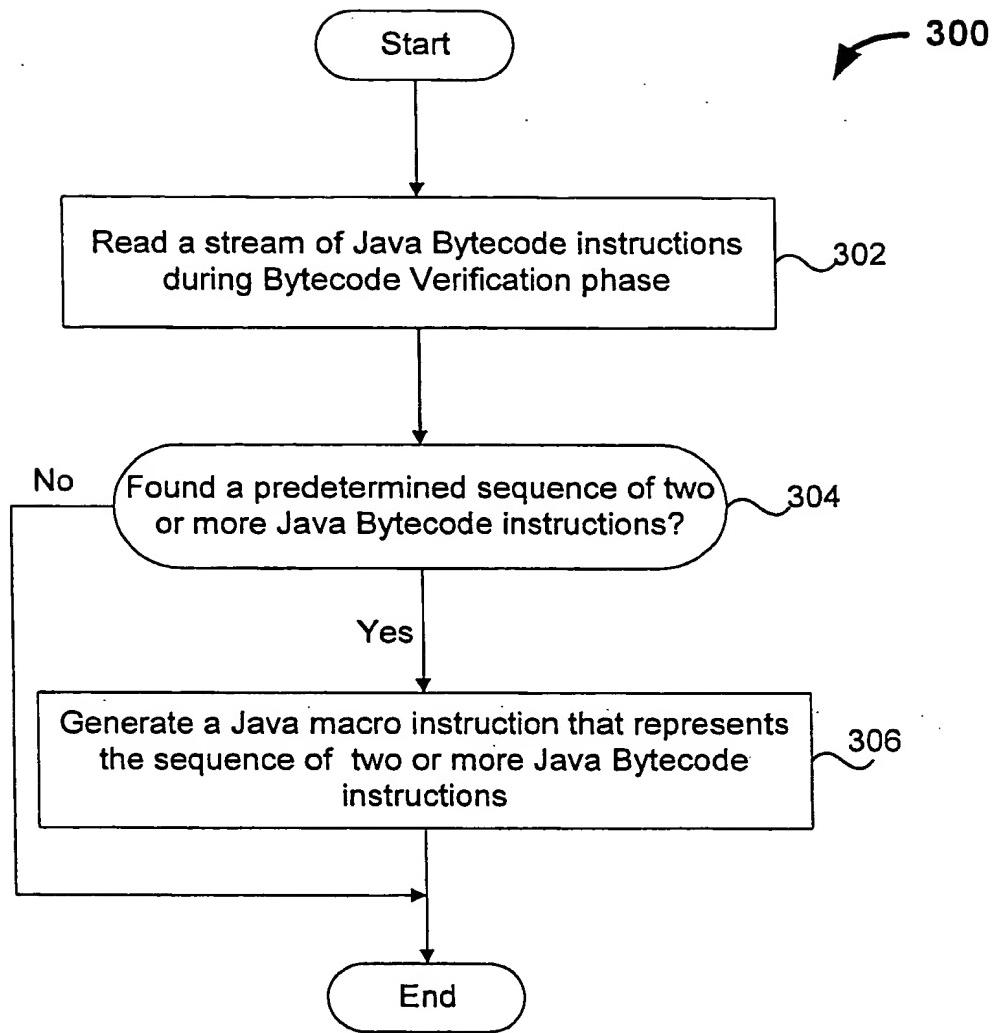


Fig. 3

00000000000000000000000000000000

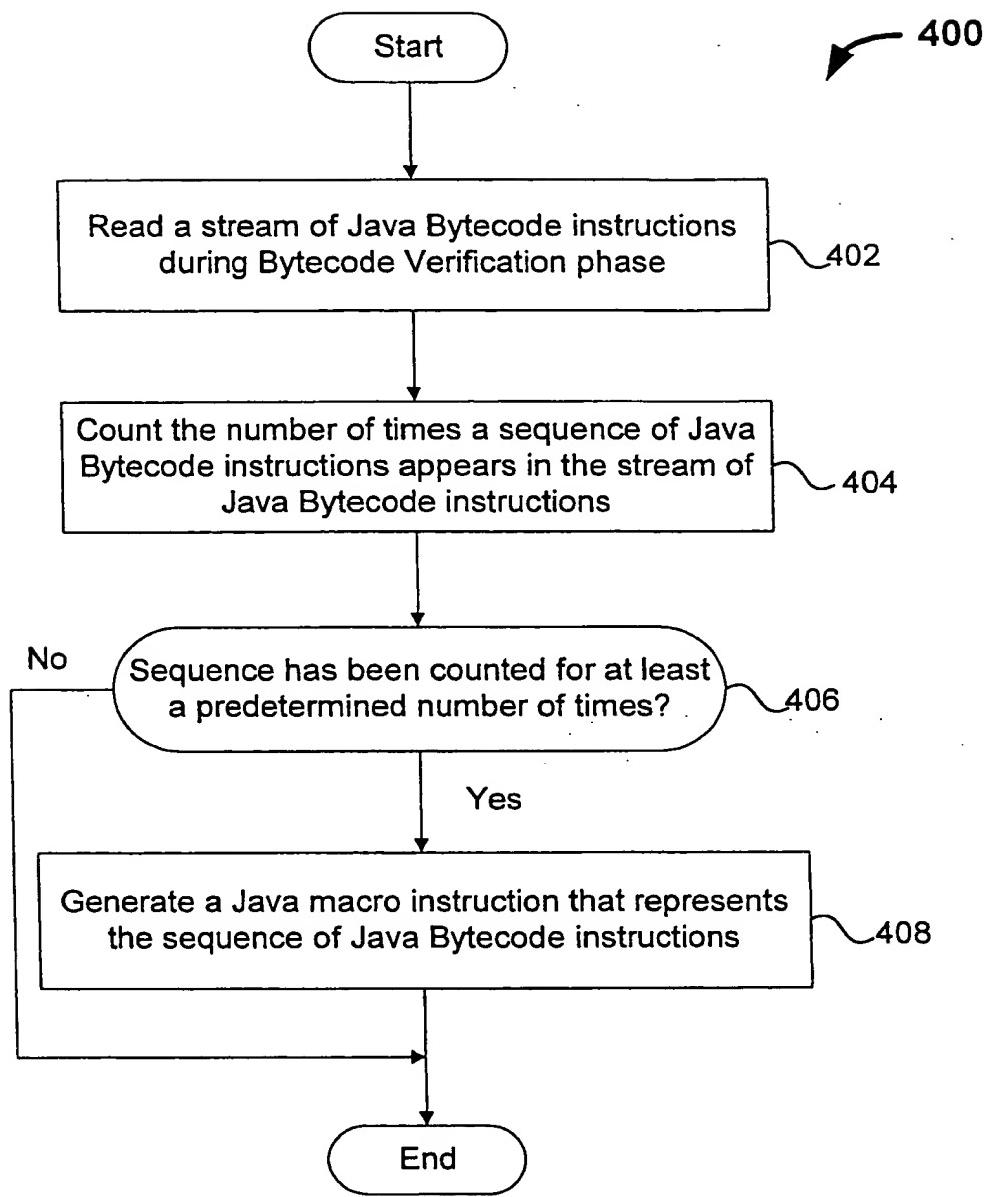


Fig. 4

10250 = 22162660

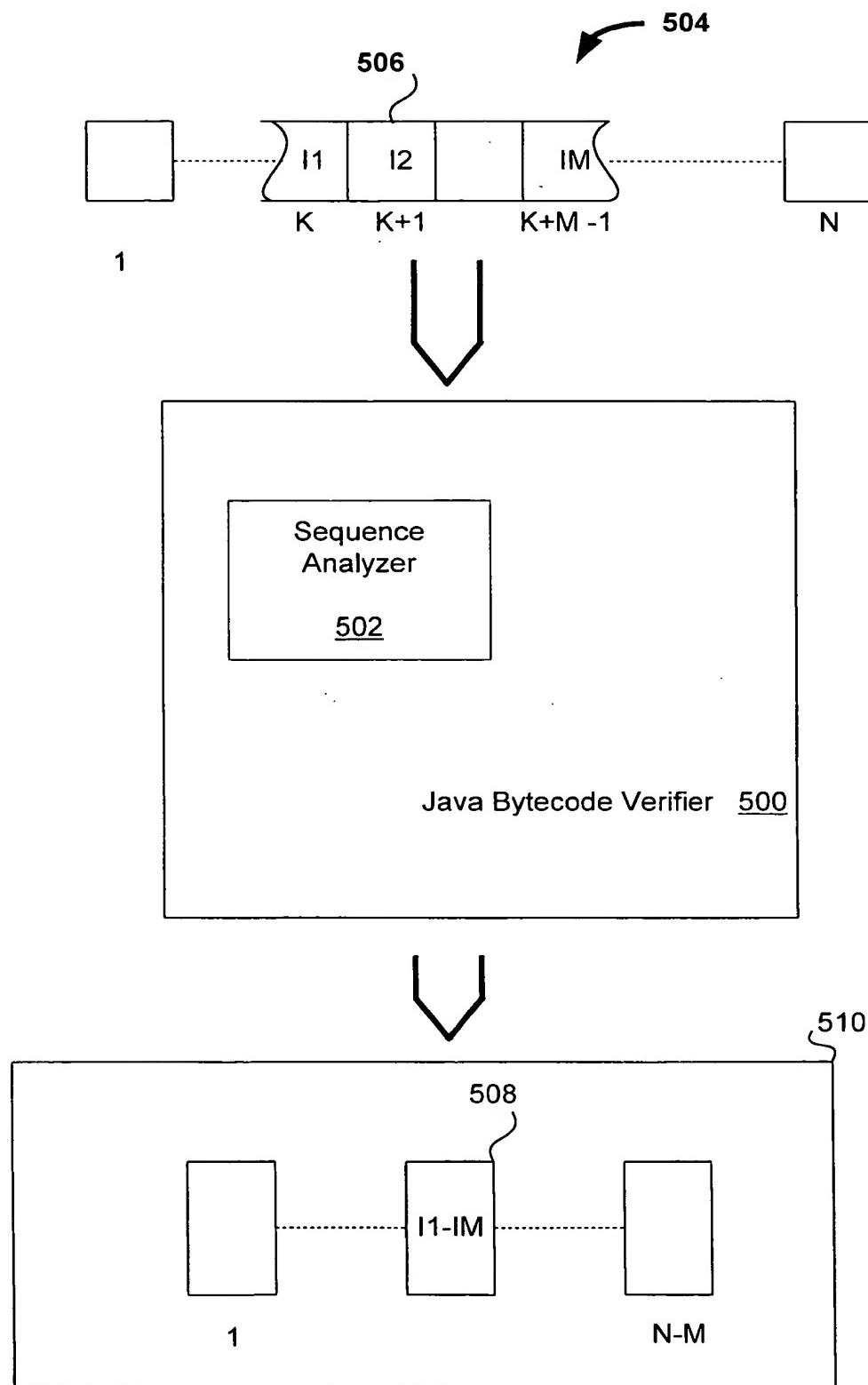


Fig. 5

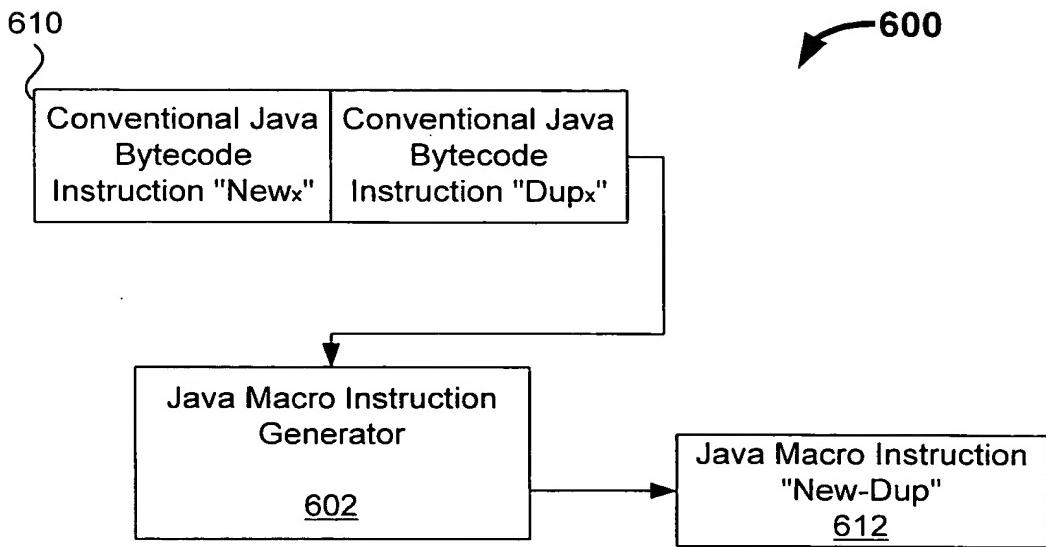


Fig. 6A

610 600 612 602 620 630

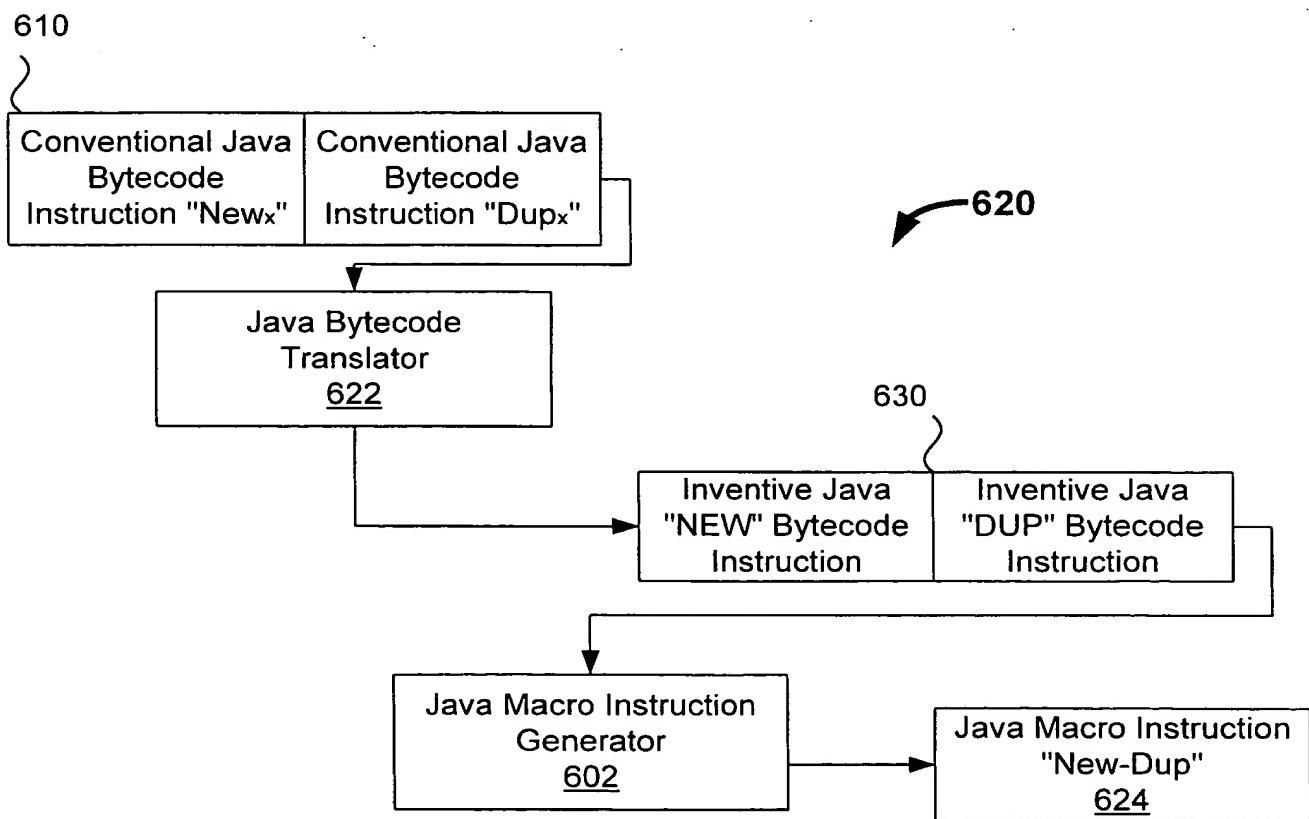


Fig. 6B

Memory Stack Diagram

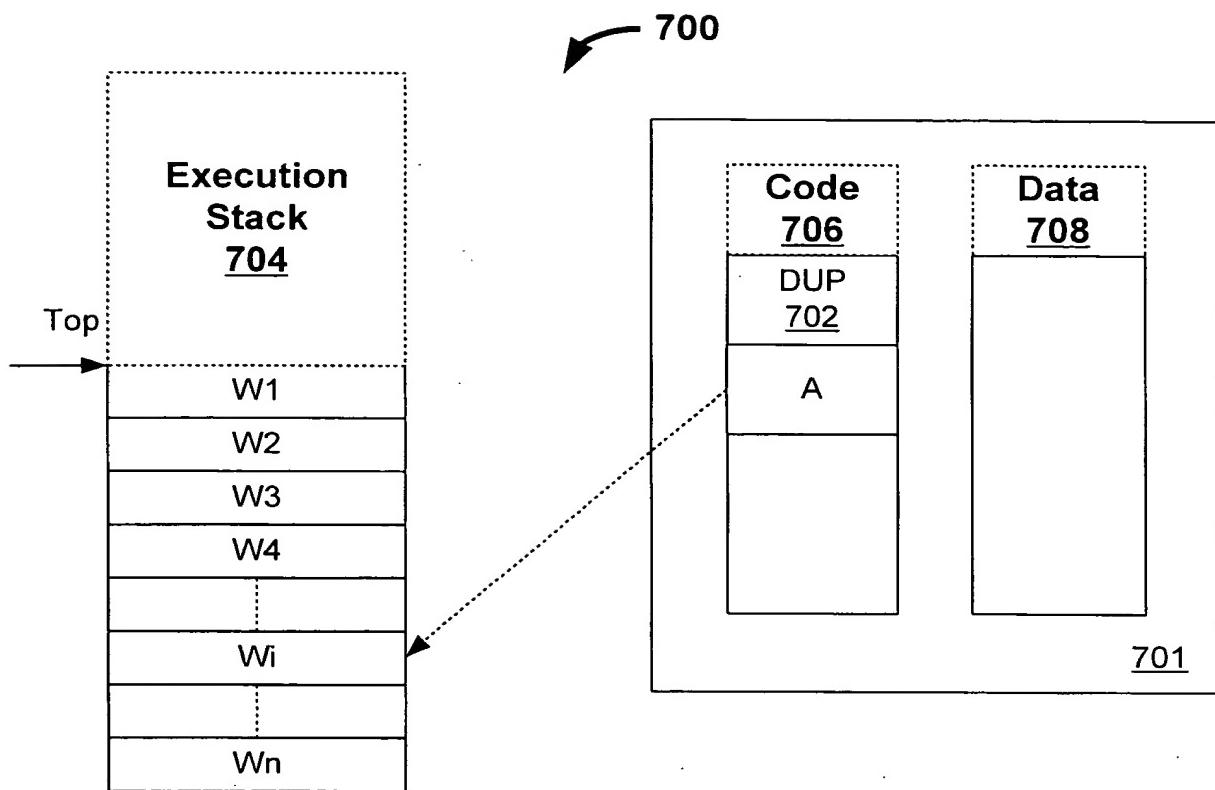


Fig. 7A

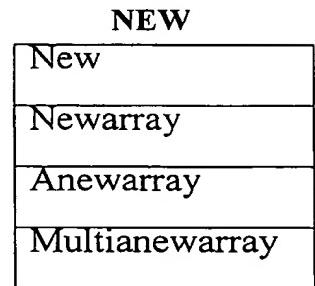
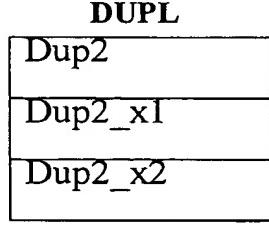
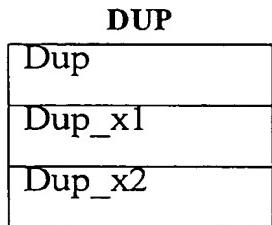


Fig. 7B

Fig. 7C

Fig. 8

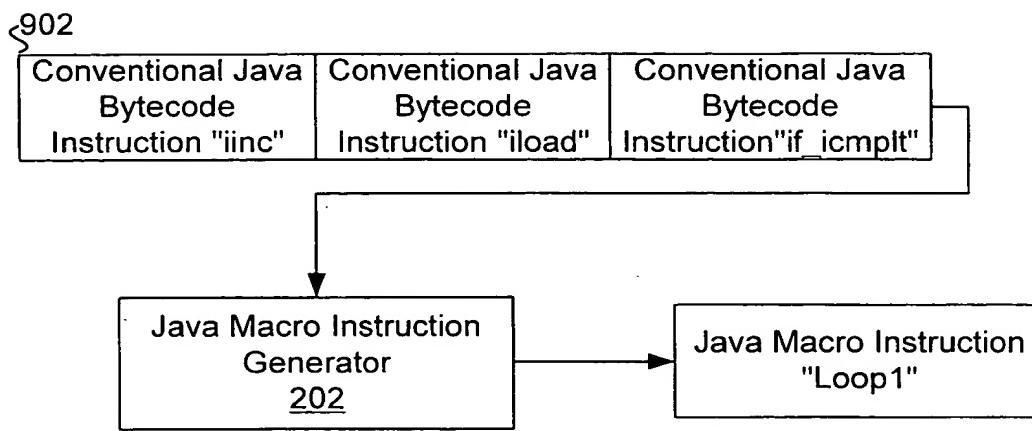


Fig. 9A

TOTAL NUMBER OF INSTRUCTIONS

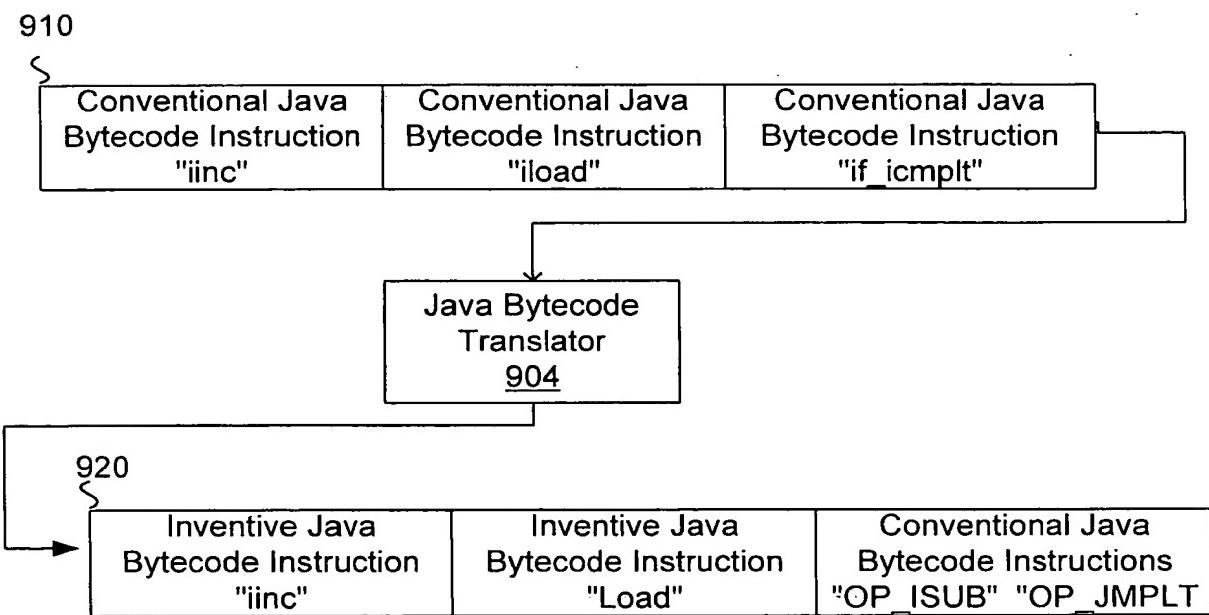


Fig. 9B

1000

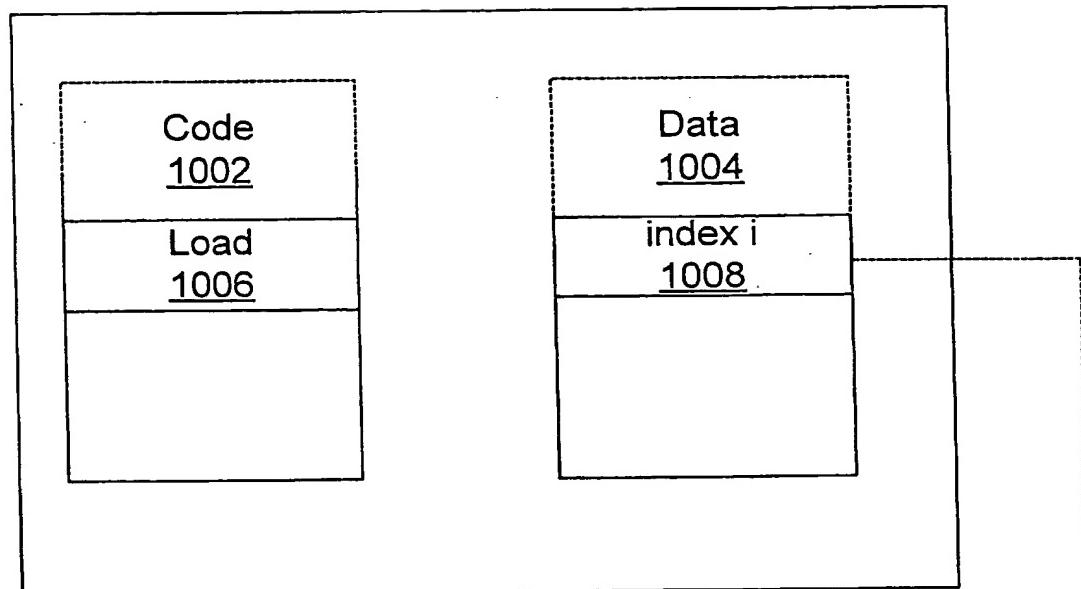
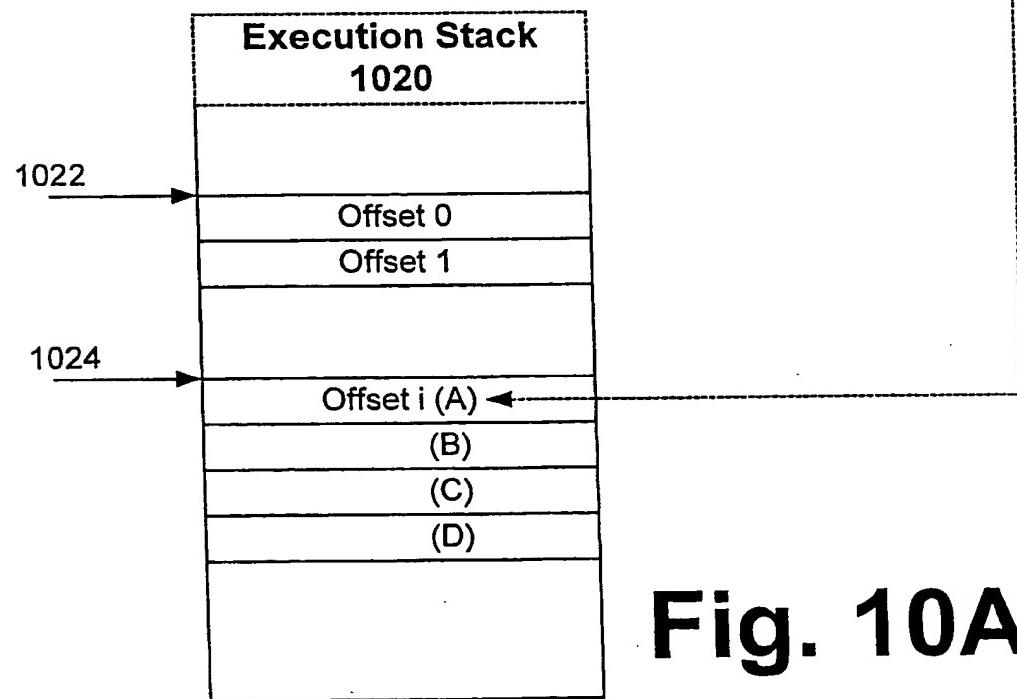


Fig. 10A



00000000000000000000000000000000

LOAD
iload
fload
aload
iload_0
iload_1
iload_2
iload_3
fload_1
fload_2
fload_3
aload_0
aload_1
aload_2
aload_3

Fig. 10B

LOADL
lload
dload
lload_0
lload_1
lload_2
lload_3
fload_0
dload_0
dload_1
dload_2
dload_3

Fig. 10C

lcmp	OP_LSUB, OP_JMPEQ
fcmpl	OP_FSUB, OP_JMPLE
fcmpg	OP_FSUB, OP JMPGE
dcmpl	OP_DCMP, OP_JMPLE
dcmpg	OP_DCMP, OP JMPGE

Fig. 11A

Digitized by srujanika@gmail.com

if_icmpeq	OP_ISUB, OP_JMPEQ
if_icmpne	OP_ISUB, OP_JMPNE
if_icmplt	OP_ISUB, OP_JMPLT
if_icmpge	OP_ISUB, OP JMPGE
if_icmpgt	OP_ISUB, OP JMPGT
if_icmple	OP_ISUB, OP_JMPLE
if_acmpeq	OP_ISUB, OP_JMPEQ
if_acmpne	OP_ISUB, OP_JMPNE

Fig. 11B

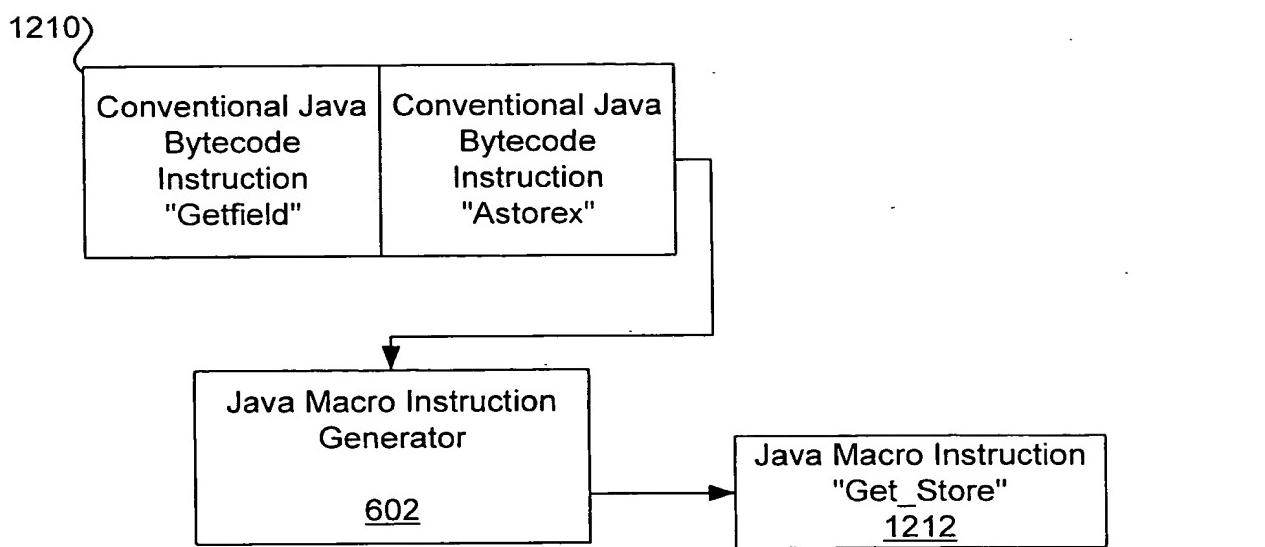


Fig. 12A

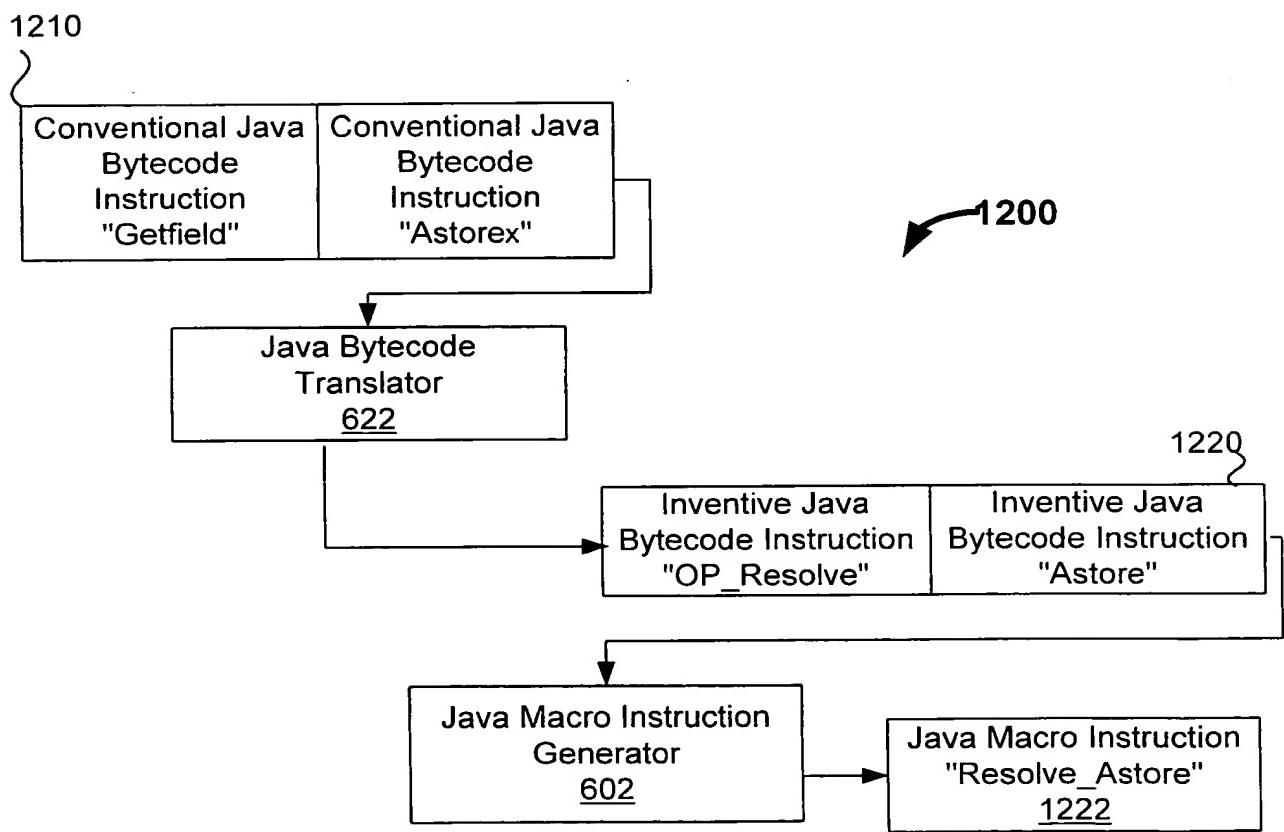


Fig. 12B

00000000000000000000000000000000

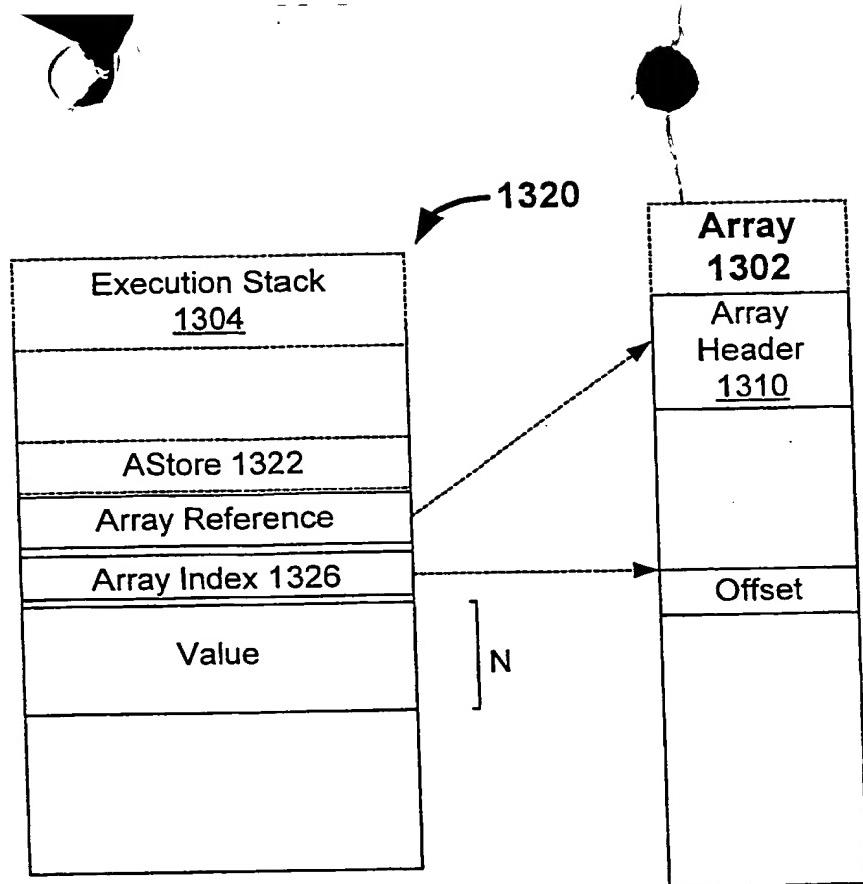


Fig. 13A

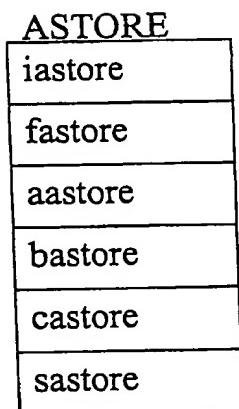


Fig. 13B

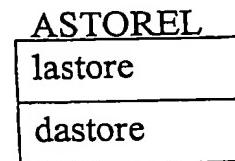


Fig. 13C